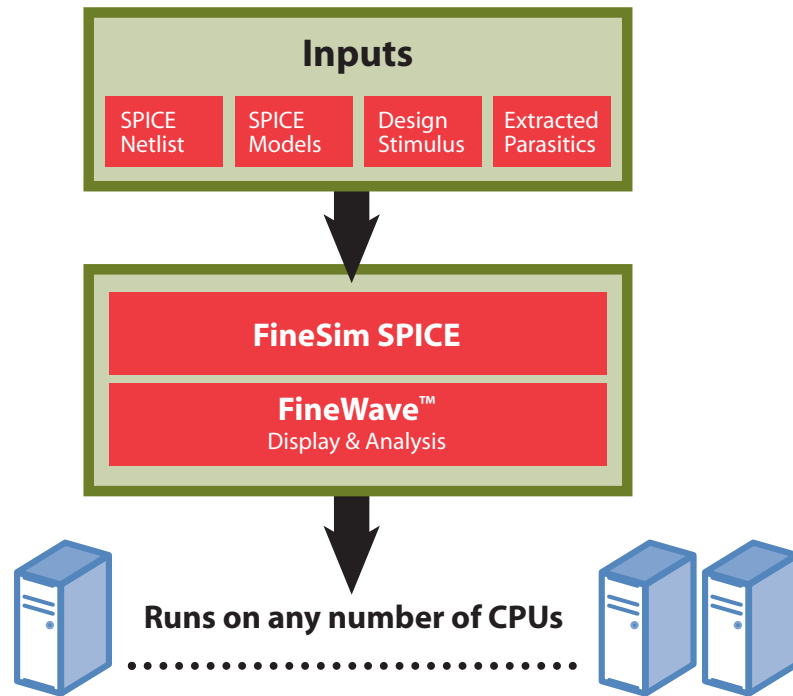


FineSim™ SPICE

- Multi-CPU simulation enabled through Magma's Native Parallel Technology™ delivers silicon-accurate results for analog circuits. This revolutionary technology enables true SPICE simulation over multiple CPUs and provides linear scaling for both performance and capacity.
- Provides unmatched performance through improved SPICE solvers, improving runtimes by 3X to 10X for single-CPU simulations. With multi-CPU simulations the performance scales linearly per CPU and can increase 20X to 30X or more in some cases.
- Achieves silicon-accurate results for all mixed-signal designs and analog intellectual property (IP) that typically present simulation closure challenges, including PLLs, ADCs, charge pumps and memory IP.

FineSim SPICE defines a new paradigm in circuit simulation, enabling simulation of the most challenging analog/mixed-signal designs with SPICE accuracy and unprecedented performance.



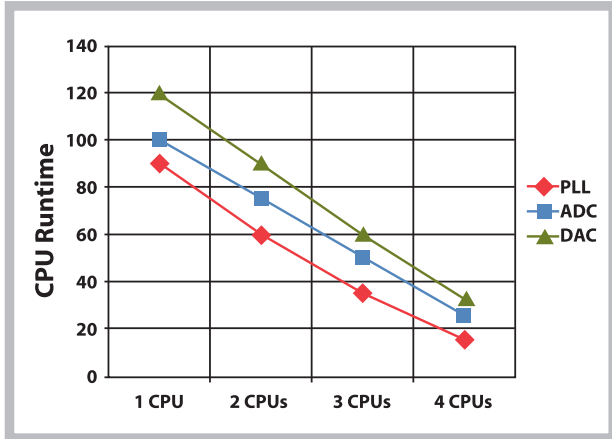
Analog designers face many challenges in verifying their designs with traditional SPICE tools. Many designs require several days or even weeks to converge on an accurate solution. Also due to increasing design complexity, designers need to simulate multiple analog blocks or subsystems with SPICE, but can't because of capacity limitations of traditional SPICE simulators.

FineSim SPICE enables analog designers to simulate analog designs that could never before be analyzed in SPICE. With FineSim SPICE's Native Parallel Technology, designers can simulate their analog IP, mixed-signal subsystems and large memory arrays in full SPICE.

FineSim SPICE

Faster Simulation than Traditional SPICE

With an improved architecture and better numerical solvers, on a single CPU, FineSim SPICE delivers a 3X to 10X performance increase over existing commercial SPICE simulators. When multiple CPUs are used, FineSim SPICE's simulation performance can exceed 30X performance compared to other commercially-available SPICE products that use a single CPU.



Parallel simulation speed-up gain: FineSim SPICE produces silicon-accurate results on multi-CPU machines while improving simulation performance on the order of 10X to 30X or more.

Superior Simulation Capacity

FineSim SPICE with Native Parallel Technology enables analog designers to simulate large analog designs and achieve silicon accuracy never before possible in SPICE. Using multiple CPUs, FineSim SPICE has accurately simulated designs with more than a million transistors in full SPICE and matched measurements taken from silicon.

Design Type	#MOS	#CPUs
Design A	150K	20
Design B	290K	16
Design C	300K	32
Design D	350K	20
Design E	600K	32
Design F	1,200K	20

Increased design capacity: FineSim SPICE produces silicon-accurate results on multi-CPU machines for very large designs.

TECHNOLOGY FEATURES:

- Native Parallel Technology increases performance and capacity
- Supports industry-standard netlist formats: HSPICE, Spectre and Eldo
- Standard output formats for data analysis (TRO, FSDB, WDF)
- Performs DC, transient, AC and Monte Carlo analysis
- Integrated with SiliconSmart® library characterization products
- Integrated with Quartz™ Rail for more accurate cell-level power analysis



1650 Technology Drive, San Jose, CA 95110 USA | Tel: 408-565-7500 | Fax: 408-565-7501 | www.magma-da.com